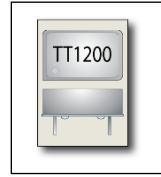


# TT-VT 1200 TCXO/VCTCXO

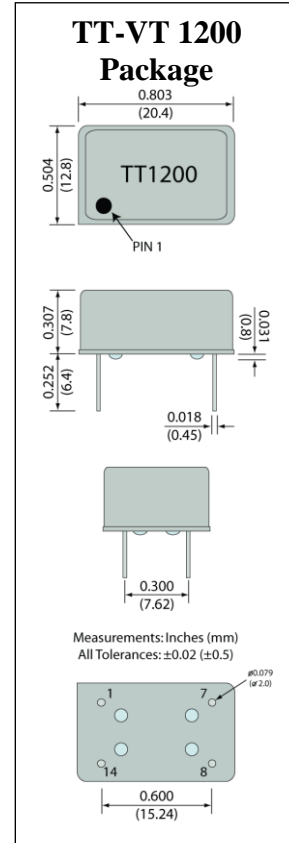


**FEATURES:**

**High Precision  
Thru-Hole Metal Can**

**CMOS and Clipped Sine  
20.4 x 12.8 x 7.8 mm**

Parameter	Unit	Min.	Max.
Frequency Range	MHz	10	40
Frequency Tolerance at 25°C	ppm	-	±2.0
Frequency Stability			
vs. Supply Voltage (±5%) change	ppm	-	±0.02
vs. Load (±10%) change	ppm	-	±0.3
vs. Aging	ppm	-	±1.0
Storage Temperature Range	°C	-55	+125
Current Consumption	mA	10 max. (5.0 V); 15 max. (3.3 V)	
Load (CMOS)	pF		15
Load (Clipped Sine)			10 KOhms//10pF
Output Level (CMOS)	V	90%	10%
Output Level (Clipped Sine)	V p-p	0.8	-
Duty Cycle (CMOS only)	%		45/55
Voltage			3.3, 5.0 ±5%
Output Level	Vp-p	0.8	-
Load			10KOhms//10pF
Frequency Adjustment (Trimmer)	ppm	±3.0	-
Control Voltage Range (VCTCXO)	V	0.5	2.5
Frequency Deviation (VCTCXO)	ppm	±5	-
Rise and Fall Time (CMOS Only)	ns	-	6
Start-up Time	mSec	-	2
Phase Noise @ 10 MHz			
	100 Hz	dBc/Hz	-123 typical
	1 kHz	dBc/Hz	-143 typical
	10 kHz	dBc/Hz	-150 typical



**Frequency Stability vs. Temperature Range**

Temperature	Stability (ppb)
-20 to +70°C	±30, ±50, ±100
-40 to +85°C	±50, ±100
-40 to +105°C	±100

**Environmental**

Terminal Material	KOVAR
Terminal Plating	Sn-Ag-Cu
REACH Compliant	Yes
RoHS Compliant	Yes
RoHS Exemptions	No
Re-flow Temp. Max.	260°C
MSL	1



**Example Part Number: VT1200-A-18-A-27-24M576**

VT1200	-	-	-	-	-	-
1	2	3	4	5	6	
Stability	Waveform	Voltage	Pull Range	Temp. Range	Frequency	
A = ±1.5 B = ±2.0 C = ±2.5 D = ±5.0	S = Clipped Sine C = CMOS	50 = 5.0 V 3.3 = 3.3 V	A = ±10 B = ±8 C = ±5 T = TCXO	16 = -10 to 60°C 27 = -20 to 70°C 41 = -40 to 105°C	Frequency in MHz i.e. 24M576 use M for decimal point	

TELLURIAN TECHNOLOGIES, INC.