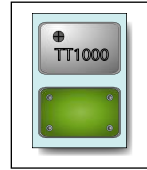


TT-VT1000 TCXO/VCTCXO

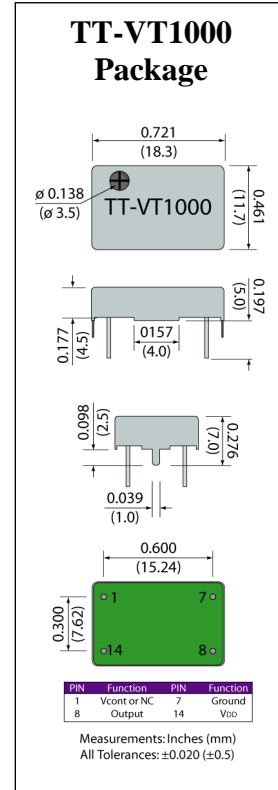


FEATURES:

- Low Cost
- Thru-Hole Metal Can

CMOS and Clipped Sine
18.3 x 11.7 x 5.0 mm

Parameter	Unit	Min.	Max.
Frequency Range (Clipped Sine]	MHz	1.2	200
Frequency Range (CMOS)	MHz	9.6	50
Frequency Tolerance at 25°C	ppm	-	±2.0
Frequency Stability			
vs. Supply Voltage (±5%) change	ppm	-	±0.3
vs. Load (±10%) change	ppm	-	±0.3
vs. Aging	ppm	-	±1.0
Storage Temperature Range	°C	-55	+125
Current Consumption (CMOS)	mA	20	60
Current Consumption (Clipped Sine)	mA	2	5
Load (CMOS)	pF	15	
Load (Clipped Sine)		10 KOhms//10pF	
Output Level (CMOS)	V	90%	10%
Output Level (Clipped Sine)	V p-p	0.8	-
Duty Cycle (CMOS only)	%	45/55	
Voltage		3.3, 5.0 ±5%	
Output Level	Vp-p	0.8	-
Load		10KOhms//10pF	
Frequency Adjustment (Trimmer)	ppm	±3.0	-
Control Voltage Range (VCTCXO)	V	0.5	2.5
Frequency Deviation (VCTCXO)	ppm	±5	±10
Rise and Fall Time (CMOS Only)	ns	-	4
Start-up Time	mSec	-	2



Frequency Stability vs. Temperature Range

Temperature	Stability (ppm)
-10 to 60°C	±1.5, ±2.0, ±2.5, ±5.0
-20 to 70°C	±2.0, ±2.5, ±5.0
-40 to 85°C	±2.5, ±5.0



Environmental

Terminal Material	KOVAR
Terminal Plating	Sn-Ag-Cu
REACH Compliant	Yes
RoHS Compliant	Yes
RoHS Exemptions	No
Re-flow Temp. Max.	260°C
MSL	1

Example Part Number: VT1000-A-S-18-A-27-24M576

VT1000	-	-	-	-	-	-	-	-	-	-
	1	2	3	4	5	6				
	Stability	Waveform	Voltage	Pull Range	Temp. Range	Frequency				
	A = ±1.5	S = Clipped Sine	50 = 5.0 V	A = ±10	16 = -10 to 60°C	Frequency in				
	B = ±2.0	C = CMOS	3.3 = 3.3 V	B = ±8	27 = -20 to 70°C	MHz i.e.				
	C = ±2.5			C = ±5	48 = -40 to 85°C	24M576 use				
	D = ±5.0			T = TCXO		M for decimal				
						point				